

AMENDMENTS TO THE CLAIMS:

Please amend the specification as follows:

1. (Currently Amended) An active matrix type liquid crystal display device comprising:

pixel electrodes that are arranged in a matrix and that are driven by pixel transistors respectively;

a plurality of gate lines that are connected, in a column-by-column fashion, to gate electrodes of the pixel transistors;

a plurality of source lines that are connected, in a row-by-row fashion, to source electrodes of the pixel transistors;

a gate driver that, sequentially during one selection period after another, connects one of the gate lines after another to an output point of a selection voltage feed circuit; and

a source driver that feeds an image signal to the source lines,

wherein the selection voltage feed circuit has:

a first power source for feeding a predetermined selection voltage; and

a diode; and

a switch,

wherein the switch is provided between the first power source and the output point of the selection voltage feed circuit, and the diode has an anode thereof connected to the second power source and having a cathode thereof connected between the output point of the selection voltage feed circuit and the switch, and

wherein the switch being kept on from a start of the selection period for a time span shorter than the selection period. ~~a second power source for feeding a voltage~~

~~lower than the predetermined selection voltage, the output point of the selection voltage feed circuit is always fed with the voltage from the second power source, and a switch is provided that so operates that, during a time span that starts at a beginning of every selection period and lasts shorter than the selection period, the output point of the selection voltage feed circuit is fed with the voltage from the first power source.~~

2. -3 (Canceled)

4. (Currently Amended) The active matrix type liquid crystal display device of ~~one of claims 1 to 3~~ claim 1.

wherein the pixel transistors are formed of amorphous silicon.

5. (Currently Amended) The active matrix type liquid crystal display device of ~~one of claims 1 to 3~~ claim 1,

wherein the selection voltage feed circuit is provided separately from the gate driver.

6. (Currently Amended) The active matrix type liquid crystal display device of ~~one of claims 1 to 3~~ claim 1,

wherein the selection voltage feed circuit is arranged, along with a low-level gate voltage source, outside the gate driver.

7. (Currently Amended) The active matrix type liquid crystal display device of ~~one of claims 1 to 3~~ claim 1,

wherein, as the switch, a plurality of switches are provided one for each gate line, in parallel with one another.

8. (New) An active matrix type liquid crystal display device comprising:

pixel electrodes that are arranged in a matrix and that are driven by pixel transistors respectively;

a plurality of gate lines that are connected, in a column-by-column fashion, to gate electrodes of the pixel transistors;

a plurality of source lines that are connected, in a row-by-row fashion, to source electrodes of the pixel transistors;

a gate driver that, sequentially during one selection period after another, connects one of the gate lines after another to an output point of a selection voltage feed circuit; and

a source driver that feeds an image signal to the source lines,

wherein the selection voltage feed circuit has a first power source for feeding a predetermined selection voltage, and a second power source for feeding a voltage lower than the predetermined selection voltage, and a switch is provided that so operates that, during a time span that starts at a beginning of every selection period and lasts shorter than the selection period, the output point of the selection voltage feed circuit is fed with the voltage from the first power source,

wherein as the switch, a plurality of switches are provided one for each gate line in parallel with one another, and

wherein, during a time span that falls within the selection period and during which the voltage from the first power source is not fed to the output point of the selection voltage feed circuit, the voltage from the second power source is fed to the output point of the selection voltage feed circuit.